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PATENT

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Pitsianis et al.

Serial No.: 10/799,316

Filed: March 12, 2004

For: METHODS AND APPARATUS FOR SINGLE STAGE GALOIS FIELD
OPERATIONS

Group: 2193

Examiner: Mai, Tan V.

Durham, North Carolina
September 12, 2007

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATION OF FACSIMILE TRANSMISSION

Sirs:

I hereby certify that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office, Fax. No. 571-273-8300 on the date set forth below

1. Response to Restriction Requirement (2 pages).

Marianna Tortorelli

Printed name of person signing

Marianna Tortorelli

Signature

Date: September 12, 2007

SEP 12 2007

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Response to Restriction Requirement

Sir:

The present response replies to the Restriction Requirement mailed August 13, 2007.

That requirement required restriction to two inventions categorized in groups as follows:

Group I. Claims 1-14 recite the method for Galois Filed multiplication having the detail generating, combining and computing steps (independent claims 1 and 4) and the multiplication circuit cell for performing the computing step of claims 1 and 4.

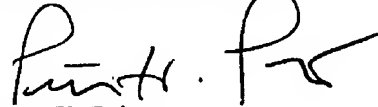
Group II. Claims 15-19 recite the apparatus/method/computer-readable medium for computing Galois Filed multiplication having m-by-m array cells.

Appl. No. 10/799,316
Amdt. dated September 12, 2007
Reply to Office Action of August 13, 2007

Applicants elect with traverse the Group I claims 1-14.

Please cancel claims 15-19 without prejudice to the presentation of these claims by way
of a divisional or otherwise.

Respectfully submitted,



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